

# Dediware Software User Manual For cFeon\_Eon SPI NOR Flash Option Bytes

Version 1.0





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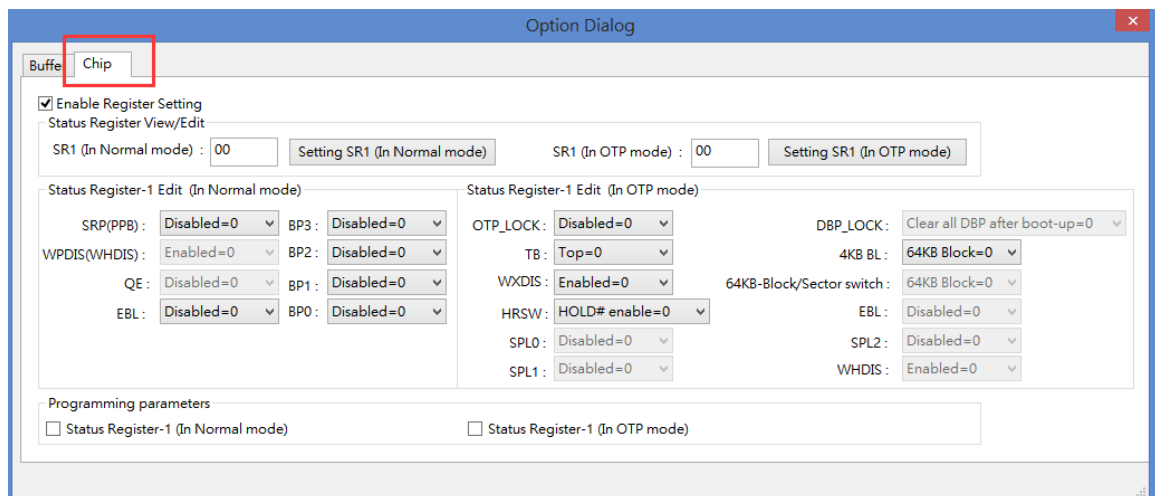
# 1 Description

This application note illustrates how to set up Dediware for programming the cFeon\_Eon SPI NOR Flash option bytes. Learn more about DediProg products and how to use them.

## 2 Read Register Value

Read IC register value by following the steps below.

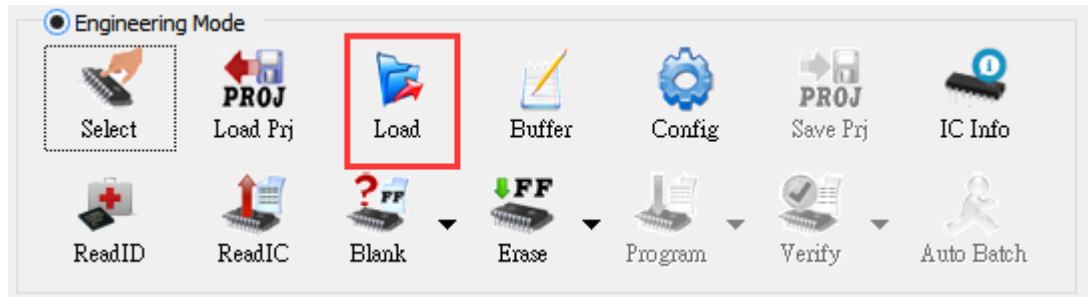
1. Click **Select** button  → Select Chip
2. Click **Read IC** button 
3. Click **Config** button
4. Click **Chip**



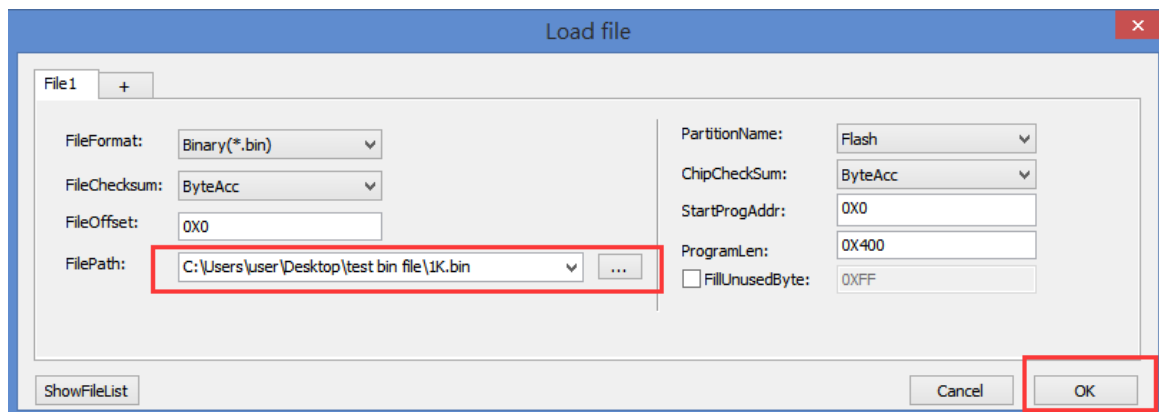
## 3 Option Bytes Setting

Please load the file before programming the register.

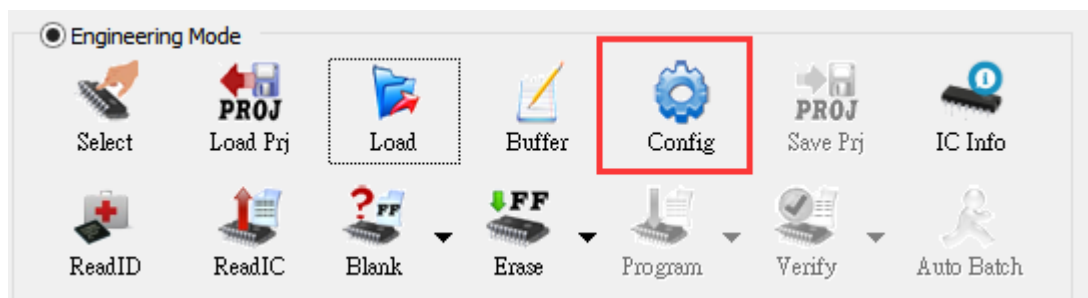
1. Click **Load** button



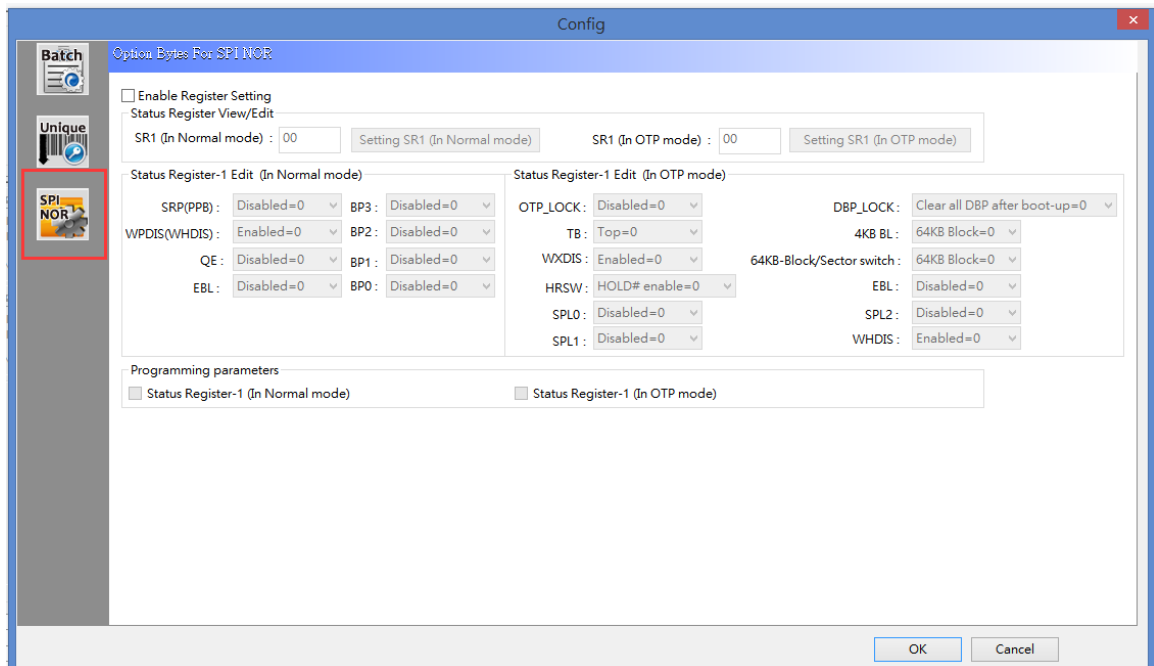
2. Load the project file → **OK**



3. Click **Config** button



4. Click **SPI NOR** button

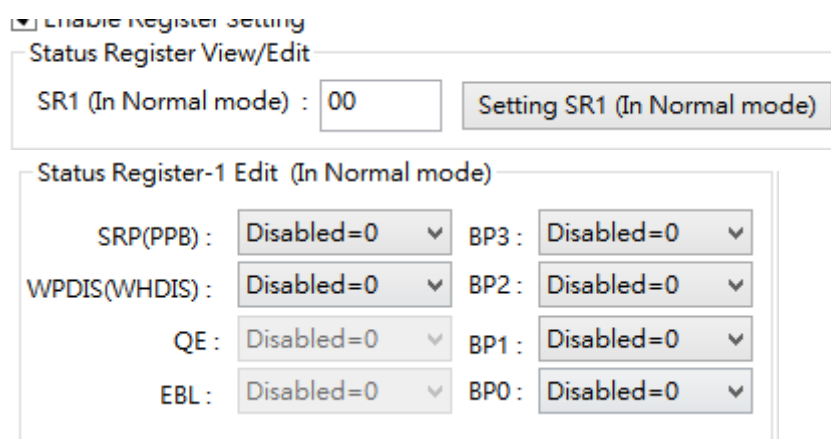


5. Set up option bytes

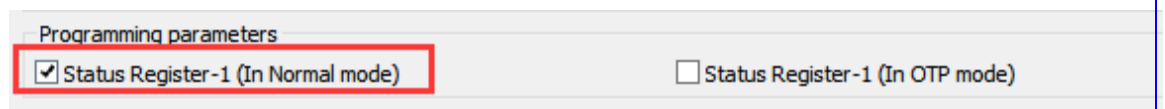
### 3.1 Status Register 1 (In Normal mode)

**Note: Program Flash or Erase Flash will erase Status Register 1 to 00h**

1. Enter the SR1 (In Normal mode) → **Setting SR1 (In Normal mode)**, or select the status for each Bit in the Status Register



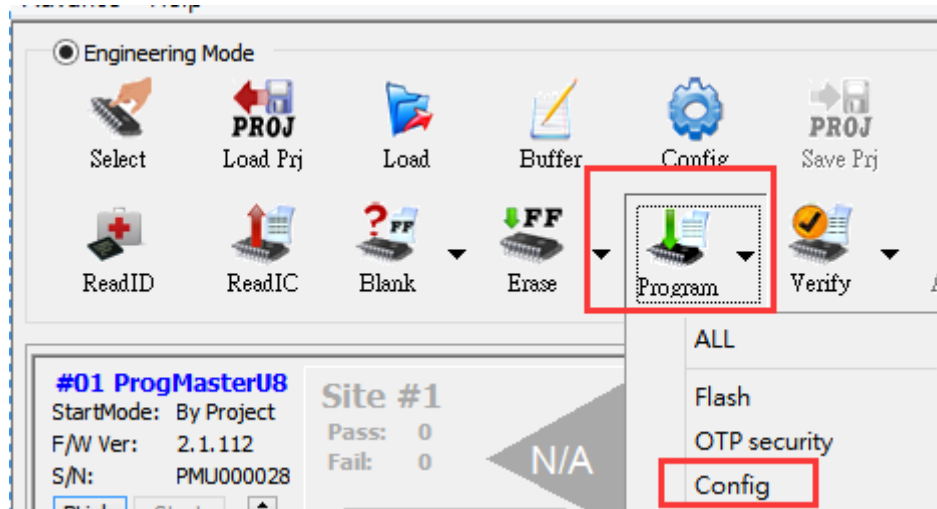
2. Programming Parameters → Check **Status Register-1 (In Normal mode)**



3. Click **OK** to save the values

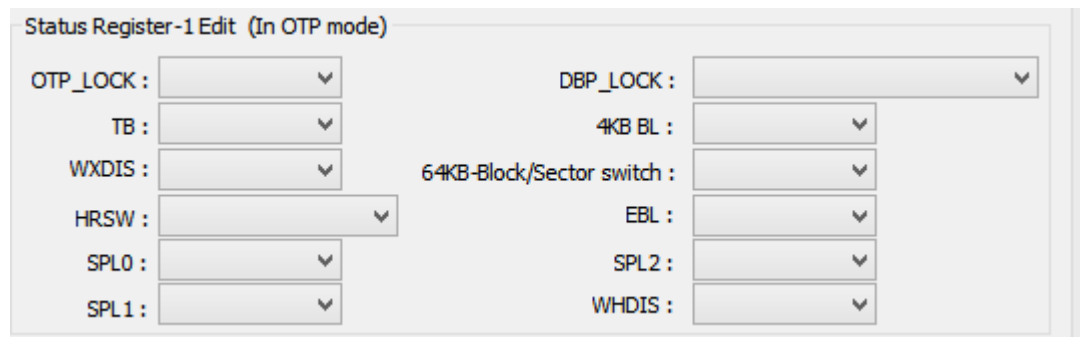
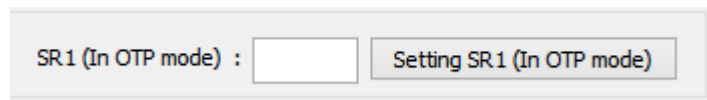


- Click **Program** button→**Config**

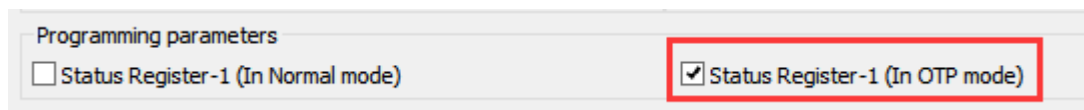


### 3.2 Status Register 1 (In OTP mode)

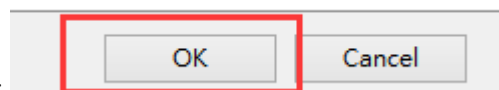
- Enter the SR1 (In OTP mode)→**Setting SR1 (In OTP mode)**, or select the status for each Bit in the Status Register



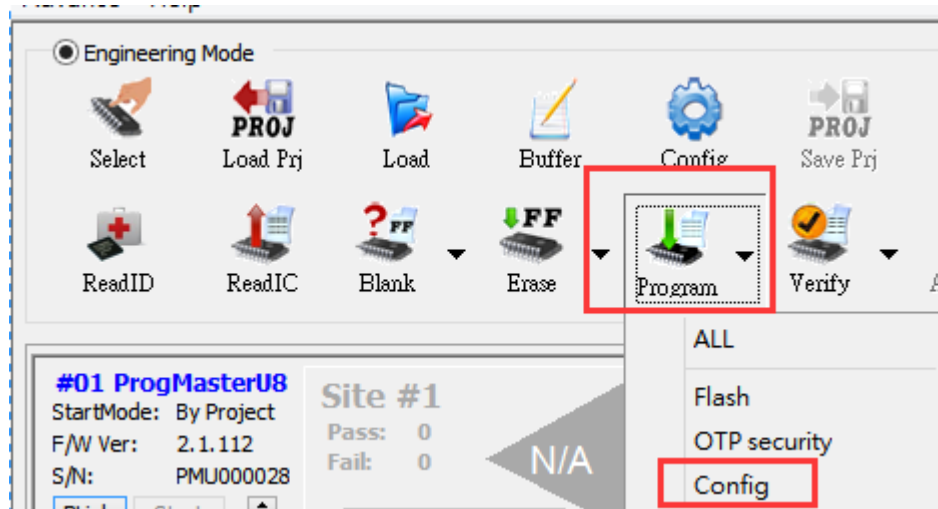
- Programming Parameters → Check **Status Register-1 (In OTP mode)**



- Click **OK** to save the values



4. Click **Program** button→**Config**



## 4 Revision History

Date	Version	Changes
2017/01/04	1.0	Initial release

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